

Access DB# 97861

SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: James Derek Ritten Examiner #: 79877 Date: 7/11/03
 Art Unit: 2122 Phone Number 30 605-5233 Serial Number: 09/708159
 Mail Box and Bldg/Room Location: SB46 Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

 Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: Program Execution Method

Inventors (please provide full names): Toshiaki Yasue, Kazunori Ogata, Kazuaki Ishizaki, Hideaki Komatsu

Earliest Priority Filing Date: 11/17/99

For Sequence Searches Only Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

Background: An interpreter is a software program designed to translate and execute code written in an interpreted language (eg. Perl or Java). Interpreted programs generally run slowly since each line has to be translated before it is executed. This contrasts with compiled code which has already been translated, and is ready for execution. "Just-In-Time" compilers have been developed which compile parts or all of ~~the~~ an interpreted language program while it is running. Optimizing Just-In-Time Compilers analyze this code to produce more efficient compiled code. Common optimization techniques include "Code Motion", "common subexpression elimination" and "privatization".

"Postdominate" is a term known in the compiler art: "A block X postdominates a block B if and only if each path from B to Exit contains the block X"

I'm generally interested in art directed at claim 1. It claims an optimization algorithm for a just-in-time compiler, ~~also~~ along with data structure for switching between the interpreted and compiled code.

Citations: Cramer et al, "Compiling Java Just In Time", IEEE Micro, 1997

• Komatsu et al, "Just In Time Compiler Technique" US Patent 6,324,686 11/2001

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Searcher Location: 4B30

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